Appl. No. 10/565,650 Docket No. Q92854 Reply to Office Action dated November 10, 2008 Replacement Sheet 1 of 12 1/12 **14**n 25n SYNCHRONIZATION POINT DETECTING SECTION TRANSMISSION PROCESSING RESPONSE 3: IEEE1394 TRANSMISSION PATH 4 BASE CYCLE COUNTER TRANSMISSION TIMING INFORMATION CYCLE_TIME 11n:CYCLE SYNCH SYNCHRONIZATION POINT DETECTING INSTRUCTION TRANSMISSION PROCESSING SECTION 2n: SLAVE 12 23n BASE CYCLE COUNTER TRANSMISSION MANAGEMENT TABLE 4 CYCLE TIME 110:CYCLE_SYNCH SYNCHRONIZATION POINT DETECTING SECTION TRANSMISSION PROCESSING RESPONSE

BASE CYCLE COUNTER
TRANSMISSION TIMING
INFORMATION

21: SLAVE

111:CYCLE_SYNCH

121

FIG. 1

120

1: MASTER

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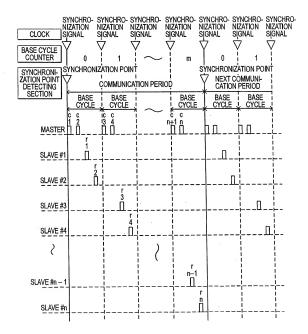
7.01.7

N MANAGEMENT TABLE		ε	2	#u-1	#	
	1	_	2	£	#	
	m+1	0	2	#	7#	
130: MASTER TRANSMISSION MANAGEMENT TABLE	TOTAL NUMBER OF CYCLES	CYCLE COUNTER	NUMBER OF INST- RUCTIONS TO TRANSMIT	DESTINATION SLAVE NO.		

- 23n: SLAVE TRANSMISSION TIMING INFORMATION m+1 231: SLAVE TRANSMISSION TIMING INFORMATION 🦳

E | E

FIG. 3



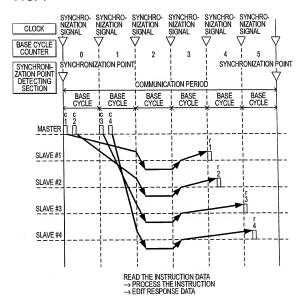
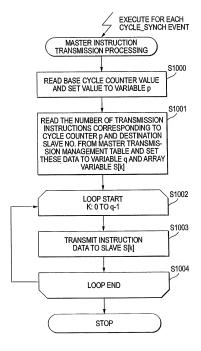
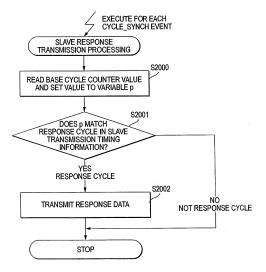


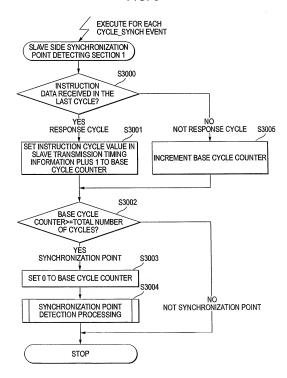
FIG. 5

SECOND_COUNT (7bit)	CYCLE_COUNT (13bit)	CYCLE_OFFSET (12bit)
(. 5.9	((,==,

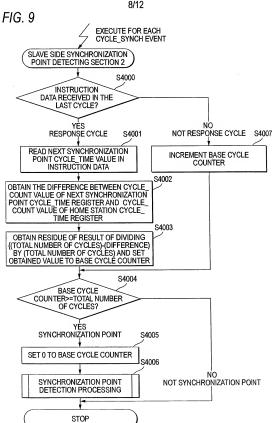
FIG. 6

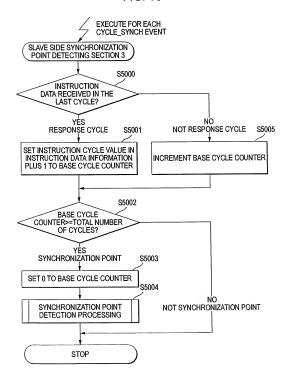






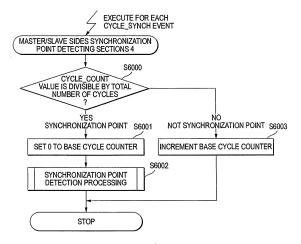
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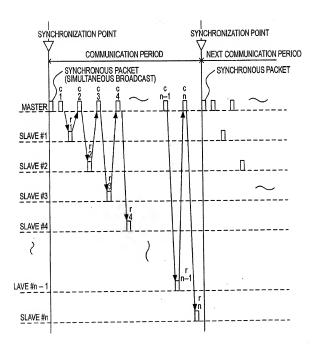


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PRIOR ART FIG. 12



PRIOR ART FIG. 13

